RegDst Branch Jump MemRead MemToReg ALUOp  MemWrite  ALUSrc  RegWrite ALU control bits

add

sub

and

or

slt

addi

ori

lw

sw

beq

j

(ALUOp is two bits)

Note: ALUOp for ori is not given in the book – use 11

You should figure our what ALUOp for addi should be

Instruction Opcode function code (for R-type)

add 000000 100000

sub 000000 100010

and 000000 100100

or 000000 100101

slt 000000 101010

addi 001000

ori 001101

lw 100011

sw 101011

beq 000100

j 000010